

PATENT  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
Earl A. Hubbell et al.

Application No.: Unassigned

Filed: Herewith

For: TECHNIQUES FOR SYNTHESIS  
INTEGRITY EVALUATION UTILIZING  
CYCLE FIDELITY PROBES

Atty. Docket No.: AFFYP007X1C1

Examiner: Unassigned

Group: Unassigned

Date: June 29, 2000

jc530 U.S. PTO  
09/607536  
06/29/00

CERTIFICATE OF EXPRESS MAIL

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR § 1.10 on the date indicated below and is addressed to: Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231

By: 

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Date of Deposit: June 29, 2000

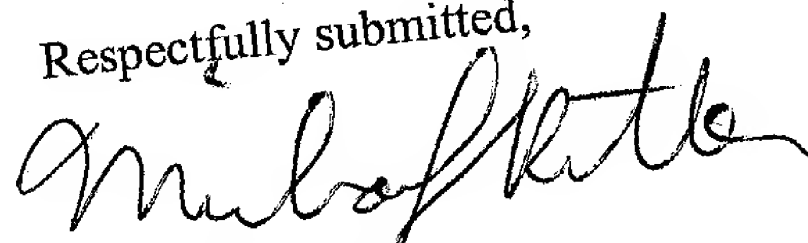
SEQUENCE LISTING REQUEST

Assistant Commissioner for Patents  
Washington, DC 20231

Dear Sir:

The subject application includes a paper Sequence Listing that is identical to the computer readable form of the Sequence Listing in parent Application No. 09/144,514. The computer readable form of the Sequence Listing was the only computer readable form submitted in the parent application. Pursuant to Rule 821(e), Applicants request that the previously submitted computer readable form that is already on file for the parent application be used for this application and are therefore not submitting a computer readable form.

Respectfully submitted,



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Attny Dkt No. AFFYP015C1